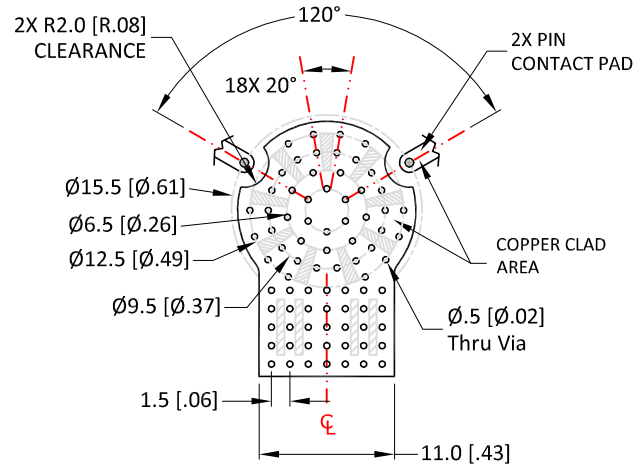
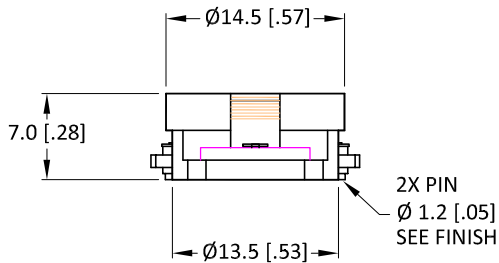
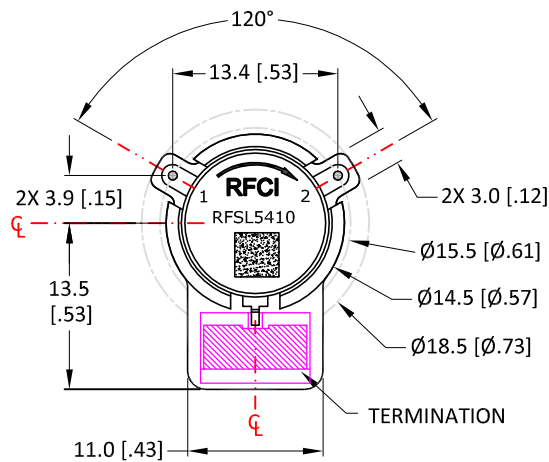


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REVISIONS

REV.	DESCRIPTION	ECO	DATE	APPROVED
A	RELEASE		06/24/15	P.T



SolderMask Pattern

Recommended Footprint Copper-Solder Mask Pattern

The Land Pattern should be with good thermal conductivity

Finish:

1. Housing: Silver plated
2. Pin: Gold plated
3. Coplanarity specification: 0.10 [.004] MAX.

Specifications

Parameter	Minimum	Typical	Maximum
Frequency Range (MHz)	1710		1785
Insertion Loss (dB)		< .25	.30
Isolation (dB)	20	> 23	
Return Loss (dB)	20	> 23	
FWD IMD: 2T at 37W per T 5MHz Spacing (dBc)		70	

NOTES:

1. Typical Values Represent Performance @ +23 °C.
2. Isolator Flange held to +85°C; 30 Min. maximum Duration.

Power & Temperature Ratings

Parameter	Maximum
Forward PWR Peak/AVG	500/80 Watts
Reverse Power CW	60 Watts
Termination Rating (See Note 2)	100 Watts
Operating Temperature	-40 to +85° C
Storage Temperature	-50 to +125° C

CW SMD ISOLATOR MODEL: RFSL5410

UNLESS OTHERWISE SPECIFIED
ALL DIMENSIONS ARE IN MILLIMETERS (INCHES):

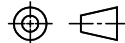
TOLERANCES ARE:
1 PLACE DECIMAL ±.2 [±.01] ANGULAR: ±1.0°
2 PLACE DECIMAL ±.10 [±.004] SURFACE ROUGHNESS 16/

REMOVE ALL BURRS AND BREAK SHARP EDGES.
SURFACE TEXTURE TO BE IN ACCORDANCE WITH LATEST ANSI B46.1
DIMENSIONING & TOLERANCING IN ACCORDANCE WITH LATEST ANSI Y14.5

PROPRIETARY NOTE: "THE INFORMATION CONTAINED ON THIS DOCUMENT IS CONSIDERED TO BE CONFIDENTIAL MATERIAL PROPRIETARY TO RF CIRCULATOR ISOLATOR Inc. (RFCI) AND IS PROVIDED SOLELY FOR INFORMATION PURPOSES.
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DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION



APPROVALS

DATE

DRAWN BY:

CHECKED BY:

DESIGN BY:

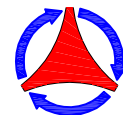
ENGINEER BY:

MFG. ENGR.

Q.A.

PROG. MGMT/MKT

RFCI



TITLE

OUTLINE/SPECS

SIZE
A

CAGE NO.

DWG NO.

SL5410-OS

REV.
A

SCALE: FULL

SHEET 1 OF 1