DWG. NO.

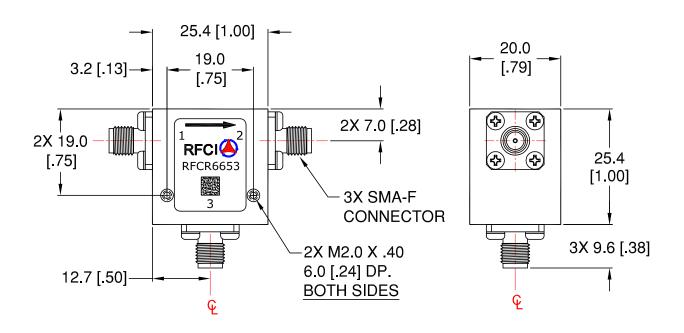
CR6653-OS

SHT REV
1 A

THIS DRAWING HAS BEEN GENERATED BY A CAD SYSTEM. CHANGES SHALL ONLY BE INCORPORATED AS DIRECTED BY THE DESIGN ACTIVITY.

| REVISIONS | | | | | | |
|-----------|-----------------|------|----------|----------|--|--|
| REV. | DESCRIPTION | ECO | DATE | APPROVED | | |
| Α | INITIAL RELEASE | I.R. | 11/15/15 | P.T | | |





Specifications

| Parameter | Minimum | Typical | Maximum |
|-----------------------|---------|---------|---------|
| Frequency Range (MHz) | 1765 | | 2250 |
| Insertion Loss (dB) | | < .40 | .550 |
| Isolation (dB) | 19 | > 21 | |
| Return Loss (dB) | 19 | > 21 | |

Power & Temperature Ratings

| Parameter | Maximum |
|-----------------------|----------------|
| Forward PWR Peak/AVG | 2.0K/200 Watts |
| Reverse Power CW | 200 Watts |
| Operating Temperature | -40 to +85° C |
| Storage Temperature | -40 to +95° C |

Notes:

- 1. Typical Values Represent Performance @ +23 °C
- 2. S-Parameters to be measured by connecting Port 1 and 2 to VNA, and Port 3 to Load with return loss 30dB or higher

SMA-f CIRCULATOR MODEL: RFCR6653

